

In the Specification:

Please add a second paragraph on page 8 as follows:

a' FIG. 2A is a block diagram showing the entire memory rewriting system according to another embodiment of the present invention;

Please replace the third paragraph on page 11 with the following:

a² The ROMs 16 and 18 can be implemented as two memories that are physically separated. Alternatively, Figure 2A illustrates the memory area of a single memory 17 may be divided into two areas so that one of the areas is used as a rewritable area, while the other is used as a non-rewritable area. In the latter case, for example, after a non-rewritable area in which a program or the like is stored has been specified in the EEPROM, a rewritable area is specified with a start address and an end address in the unfilled space of the memory.